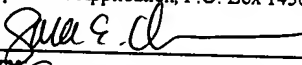


**METHODS FOR USE IN FORMING A CAPACITOR AND
STRUCTURES RESULTING FROM SAME**

Abstract of the Disclosure

A method for use with the formation of a capacitor includes providing a capacitor structure by forming a first electrode on a portion of a substrate assembly, forming a high dielectric material over at least a portion of the first electrode, and forming a second electrode over the high dielectric material. An additional layer may be formed over at least a portion of the second electrode. The portion of the substrate assembly on which the first electrode is formed and/or the layer formed over the second electrode are formed of an excess oxygen containing material.

"Express Mail" mailing label number: EV 201891012 US
Date of Deposit: February <u>23</u> , 2004
I hereby certify that the Transmittal Letter and the paper(s) and/or fee(s), as described hereinabove, are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 on the date indicated above and is addressed to the Commissioner for Patents, Mail Stop Patent Application, P.O. Box 1450, Alexandria, VA 22313-1450.
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